Attorney's Docket No.: 12732-071001 / US5156

Applicant: Jun Koyama et al. Serial No.: 09/930,956
Filed: August 17, 2001

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## <u>REMARKS</u>

Claims 1-62 are currently pending in this application with claims 1, 12, 23, 36, 50 and 62 being independent. Claims 54-61 have been withdrawn from consideration and claims 1, 12 and 62 have been amended. Support for these amendments may be found in the application at, for example, Fig. 1 and page 22, lines 14-24. No new matter has been introduced.

Claims 23-35, 36-49 and 50-53 have been provisionally rejected for obviousness-type double patenting over, respectively, claims 21-32, 33-45 and 46-48 of U.S. Application No. 09/931,061. Applicant requests that this provisional double patenting rejection be held in abeyance until the claims of the present application are otherwise found to be allowable.

Claim 62 has been rejected as being unpatentable over Okumura (U.S. Patent No. 5,945,972) in view of Kobayashi (U.S. Patent No. 4,432,610). Applicant requests reconsideration and withdrawal of this rejection because neither Okumura, Kobayashi, nor any proper combination of the two describes or suggests "a read transistor electrically connected to the first switch and to the second switch" and "a write transistor electrically connected to the third switch, to the fourth switch, and to the liquid crystal element," as recited in amended claim 62. This arrangement, which allows digital video signals to be stored in the volatile memory, and then allows the stored signals to be written in the non-volatile memory, is not shown by Okumura or Kobayashi, or by any combination of the two.

Claims 1, 4, 9-12, 15 and 20-22, including independent claims 1 and 12, have been rejected as being unpatentable over Okumura in view of Kobayashi and Perner (U.S. Patent No. 6,246,386). Applicant requests reconsideration and withdrawal of this rejection because neither Okumura, Kobayashi, Perner, nor any proper combination of the three describes or suggests read transistors, each of which is electrically connected to a corresponding first switch and a corresponding second switch, and write transistors, each of which is electrically connected to a corresponding third switch and a corresponding fourth switch, as recited in claims 1 and 12.

Dependent claims have been rejected as being unpatentable over Okumura in view of Kobayashi and Perner, and further in view of Yamazaki (U.S. Patent No. 5,699,078) (claims 2, 5, 13 and 16), Yamazaki (U.S. Patent No. 5,349,366) (claims 3 and 14), Parks (U.S. Patent No.

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5,471,225) (claims 6 and 17), Parks and Fonash (U.S. Patent No. 5,945,866) (claims 7 and 18), and Parks and Johnson (U.S. Patent No. 4,752,118) (claims 8 and 19). Applicant requests reconsideration and withdrawal of this rejection because these references, either alone or in combination, do not remedy the failure of the other references to describe or suggest the subject matter of the independent claims.

Applicant submits that all claims are in condition for allowance.

Please charge deposit account 06-1050 the amount of \$120.00 for the One-Month Extension of Time. Please apply any other charges or credits to deposit account 06-1050.

Respectfully submitted,

Date: 9/2/05

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